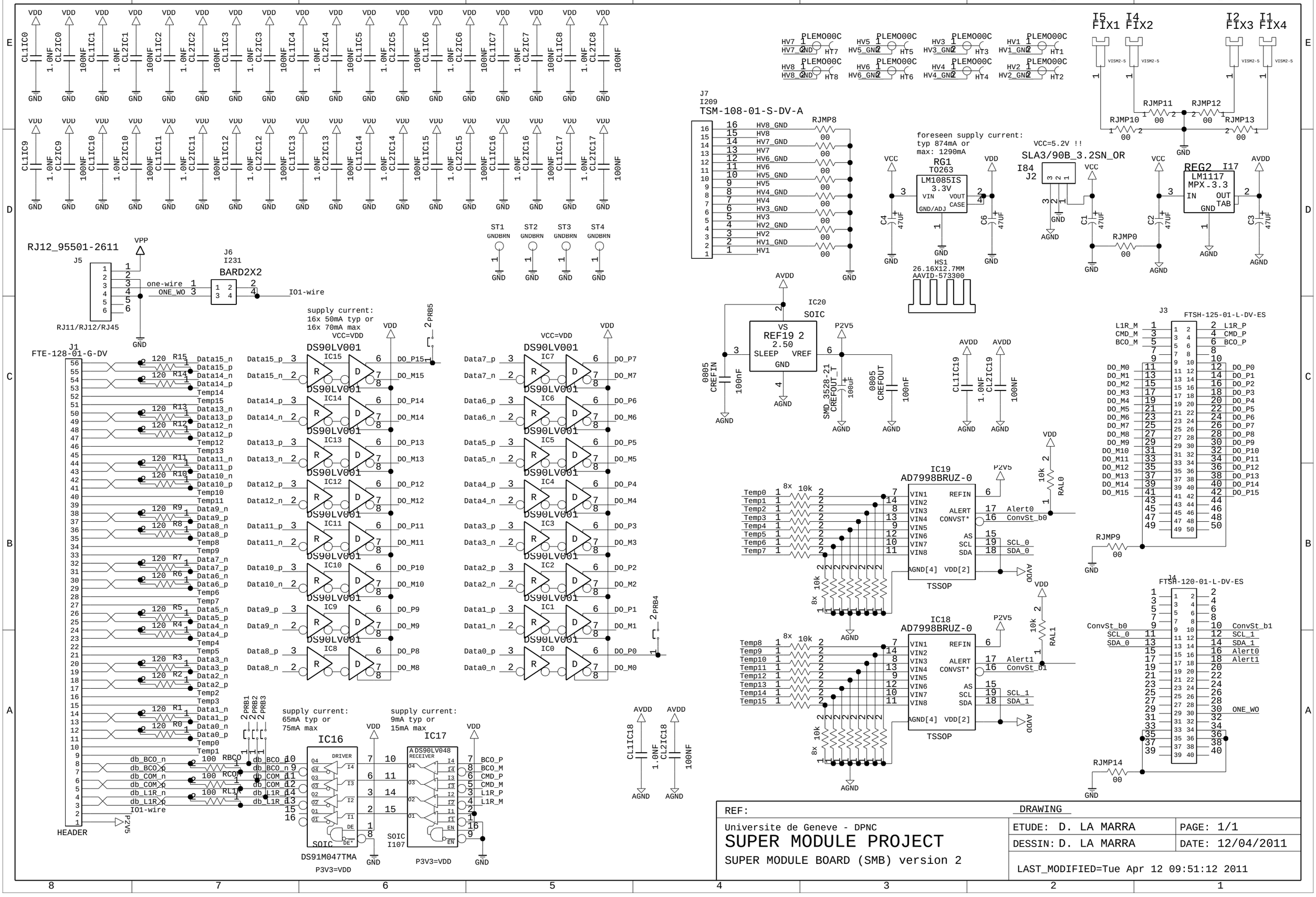
CL1IC0 1.0NF CL2IC0
CL1IC1 100NF CL2IC1
CL1IC2 1.0NF CL2IC2
CL1IC3 100NF CL2IC3
CL1IC4 1.0NF CL2IC4
CL1IC5 100NF CL2IC5
CL1IC6 1.0NF CL2IC6
CL1IC7 100NF CL2IC7
CL1IC8 1.0NF CL2IC8
CL1IC9 1.0NF CL2IC9
CL1IC10 100NF CL2IC10
CL1IC11 1.0NF CL2IC11
CL1IC12 100NF CL2IC12
CL1IC13 1.0NF CL2IC13
CL1IC14 100NF CL2IC14
CL1IC15 1.0NF CL2IC15
CL1IC16 100NF CL2IC16
CL1IC17 1.0NF CL2IC17
VDD
GND
ST1 ST2 ST3 ST4 GNDBRN
RJ12_95501-2611
J5
RJ11/RJ12/RJ45
VPP
J6 I231 BARD2X2
one-wire
ONE_WO
IO1-wire
J1 FTE-128-01-G-DV
HEADER
P2V5
R15 R14 R13 R12 R11 R10 R9 R8 R7 R6 R5 R4 R3 R2 R1 R0
Data15_n Data15_p Data14_n Data14_p Temp14 Temp15 Data13_n Data13_p Data12_n Data12_p Temp12 Temp13 Data11_n Data11_p Data10_n Data10_p Temp10 Temp11 Data9_n Data9_p Data8_n Data8_p Temp8 Temp9 Data7_n Data7_p Data6_n Data6_p Temp6 Temp7 Data5_n Data5_p Data4_n Data4_p Temp4 Temp5 Data3_n Data3_p Data2_n Data2_p Temp2 Temp3 Data1_n Data1_p Data0_n Data0_p Temp0 Temp1
supply current:
16x 50mA typ or
16x 70mA max
VCC=VDD
DS90LV001
IC15 IC14 IC13 IC12 IC11 IC10 IC9 IC8
IC7 IC6 IC5 IC4 IC3 IC2 IC1 IC0
PRB5 PRB4
PRB1 PRB2 PRB3
supply current:
65mA typ or
75mA max
IC16
DS91M047TMA
P3V3=VDD
supply current:
9mA typ or
15mA max
IC17
DS90LV048
RECEIVER
SOIC I107
db_BCO_n db_BCO_p db_COM_n db_COM_p db_L1R_n db_L1R_p
RBCO RCOM RL1R
BCO_P BCO_M CMD_P CMD_M L1R_P L1R_M
HV7_GND HV7 HV8_GND HV8 HV5_GND HV5 HV6_GND HV6 HV3_GND HV3 HV4_GND HV4 HV1_GND HV1 HV2_GND HV2
PLEMO00C
HT7 HT8 HT5 HT6 HT3 HT4 HT1 HT2
I5 I4 I2 I1
FIX1 FIX2 FIX3 FIX4
VISM2-5
RJMP11 RJMP12 RJMP10 RJMP13
J7 I209 TSM-108-01-S-DV-A
RJMP8
foreseen supply current:
typ 874mA or
max: 1290mA
RG1 TO263 LM1085IS 3.3V
VIN VOUT CASE GND/ADJ
C4 47UF C6 47UF
HS1 26.16X12.7MM AAVID-573300
VCC=5.2V !!
SLA3/90B_3.2SN_OR
I84 J2
REG2 I17 LM1117 MPX-3.3
IN OUT TAB
C1 47UF C2 47UF C3 47UF
RJMP0
AVDD
IC20 SOIC
REF19 2 2.50
SLEEP VREF GND
0805 CREFIN 100nF
SMD_3528-21 CREFOUT_T 100uF
0805 CREFOUT 100nF
CL1IC19 1.0NF CL2IC19 100NF
J3 FTSH-125-01-L-DV-ES
L1R_M CMD_M BCO_M DO_M0 DO_M1 DO_M2 DO_M3 DO_M4 DO_M5 DO_M6 DO_M7 DO_M8 DO_M9 DO_M10 DO_M11 DO_M12 DO_M13 DO_M14 DO_M15
L1R_P CMD_P BCO_P DO_P0 DO_P1 DO_P2 DO_P3 DO_P4 DO_P5 DO_P6 DO_P7 DO_P8 DO_P9 DO_P10 DO_P11 DO_P12 DO_P13 DO_P14 DO_P15
RJMP9
IC19 AD7998BRUZ-0
IC18 AD7998BRUZ-0
Temp0 Temp1 Temp2 Temp3 Temp4 Temp5 Temp6 Temp7
Temp8 Temp9 Temp10 Temp11 Temp12 Temp13 Temp14 Temp15
VIN1 VIN2 VIN3 VIN4 VIN5 VIN6 VIN7 VIN8
REFIN ALERT CONVST* AS SCL SDA
AGND[4] VDD[2]
TSSOP
RAL0 RAL1
Alert0 Alert1 ConvSt_b0 ConvSt_b1 SCL_0 SCL_1 SDA_0 SDA_1
J4 FTSH-120-01-L-DV-ES
ONE_WO
RJMP14
REF:
DRAWING
Universite de Geneve - DPNC
SUPER MODULE PROJECT
SUPER MODULE BOARD (SMB) version 2
ETUDE: D. LA MARRA
DESSIN: D. LA MARRA
PAGE: 1/1
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