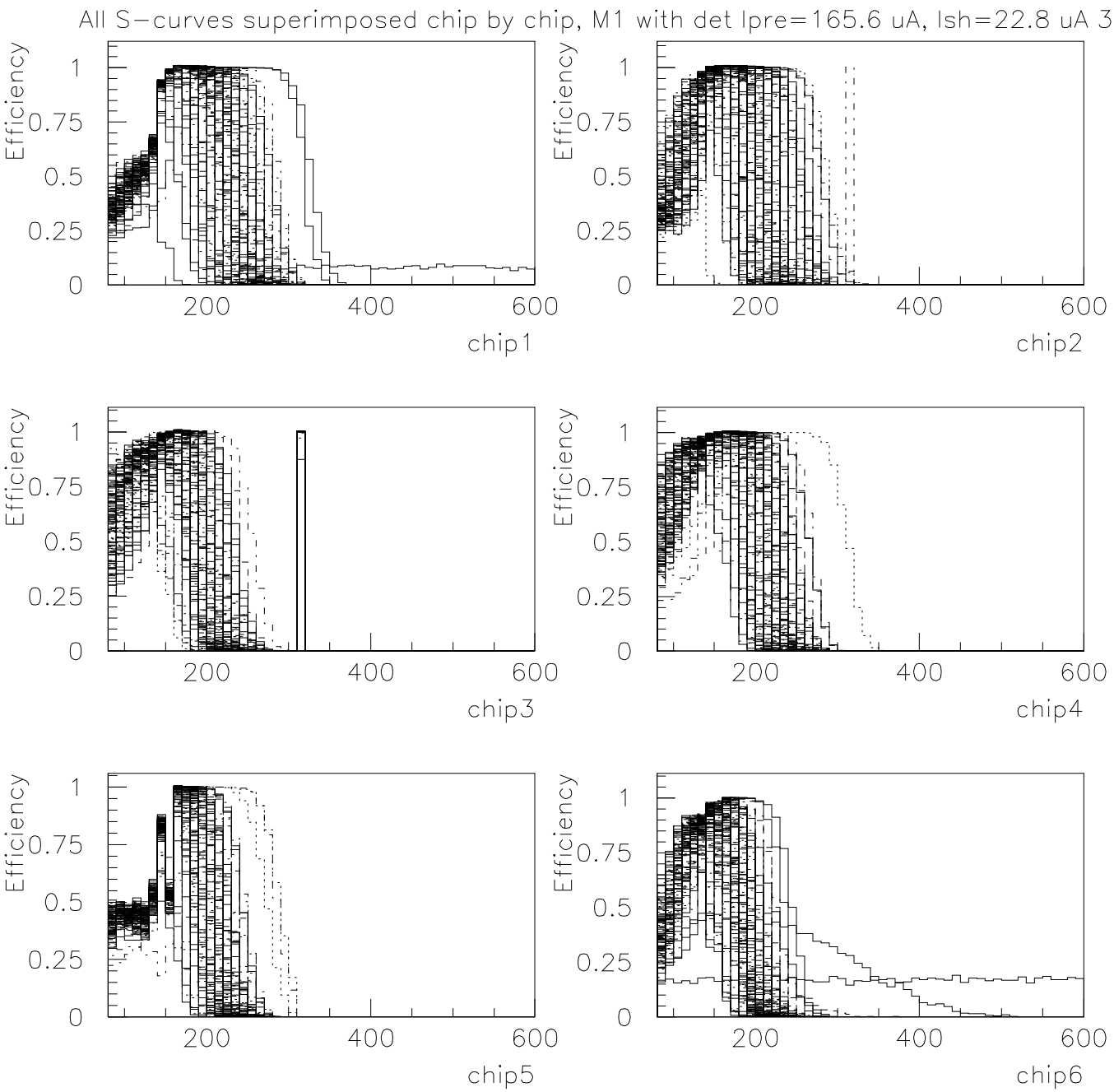
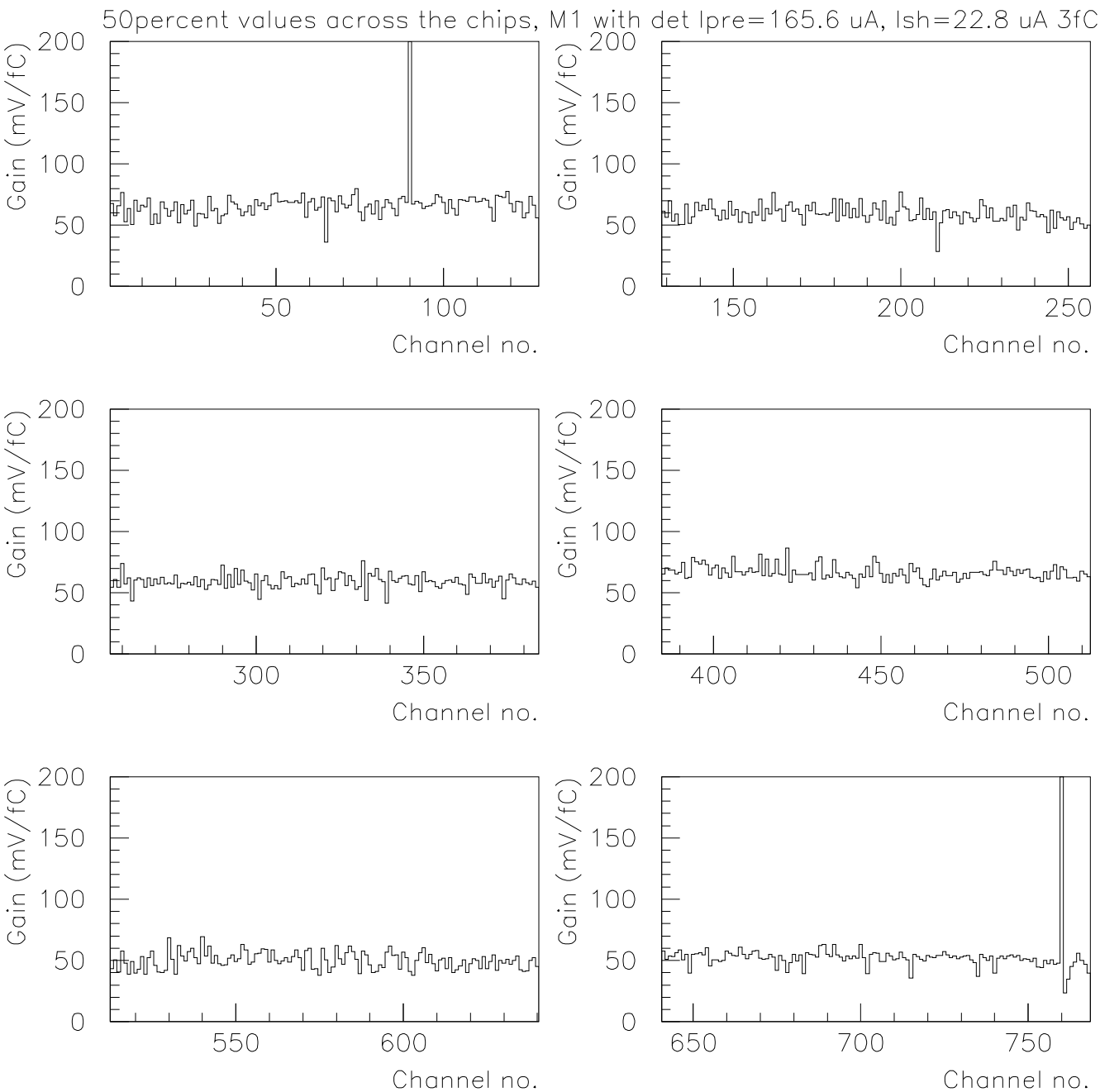
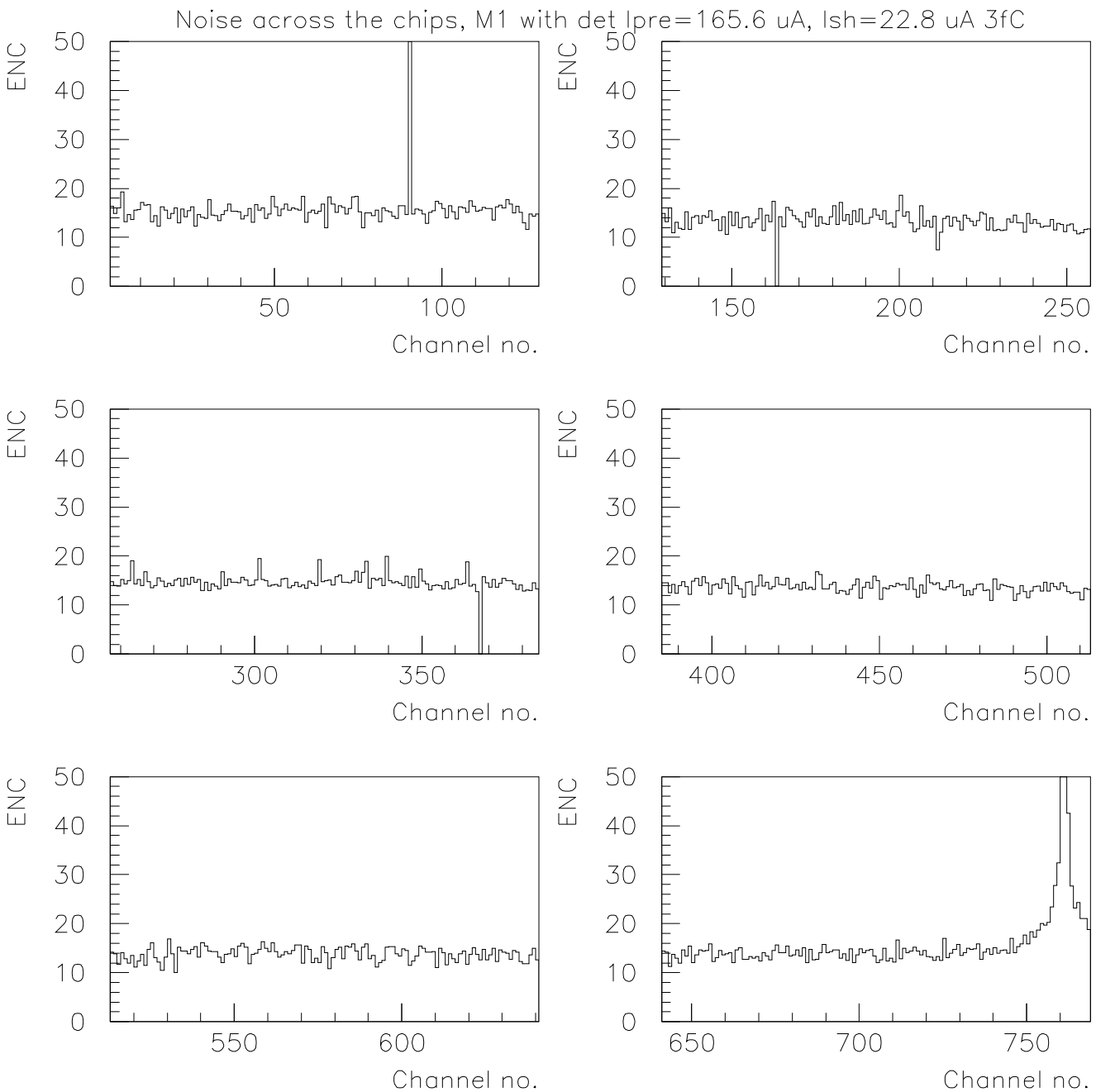
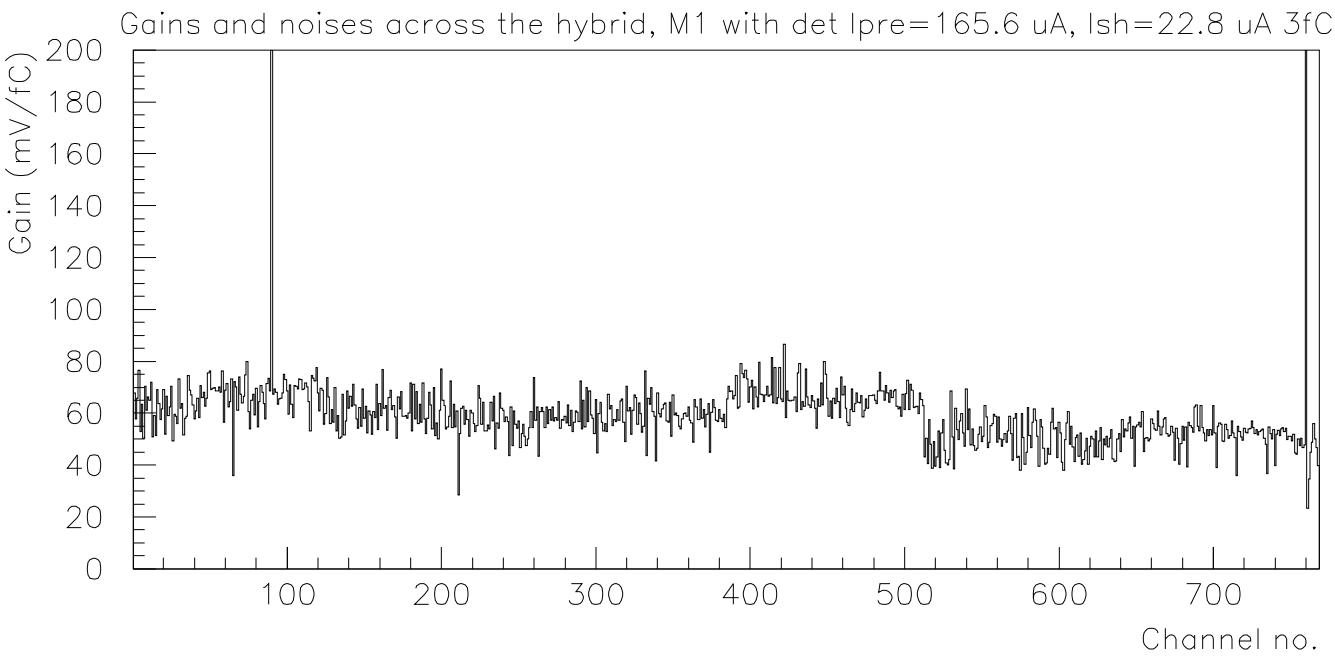
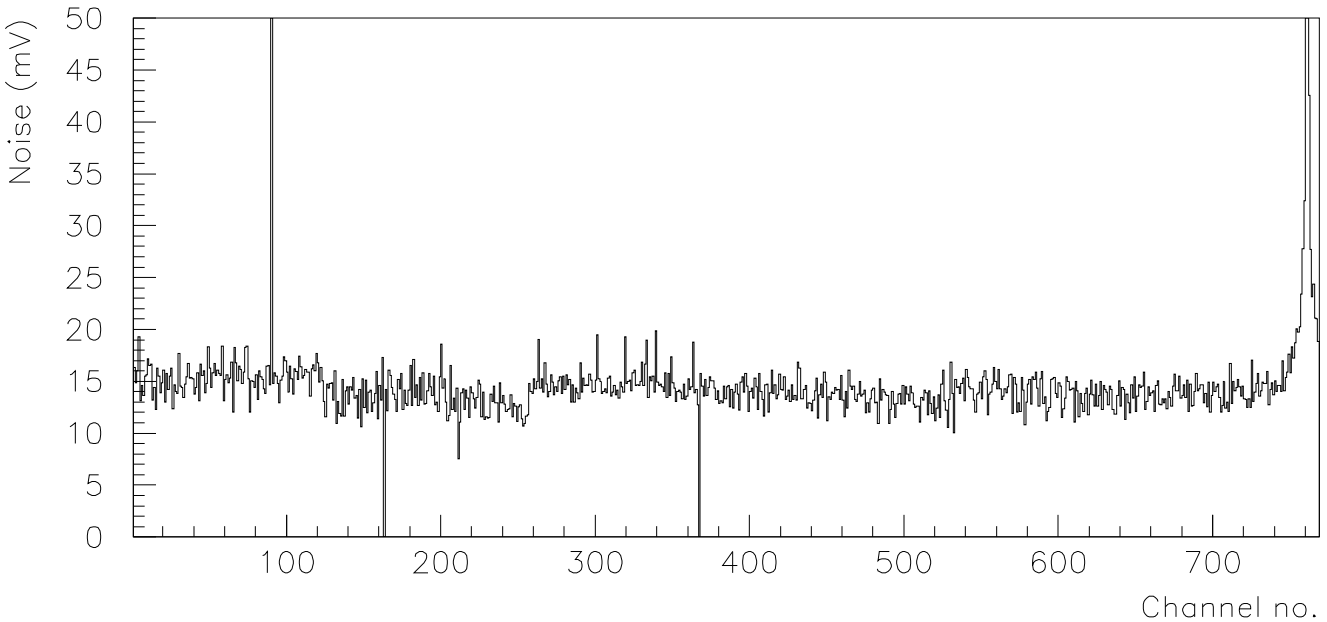

All S−curves superimposed chip by chip, M1 with det Ipre=165.6 uA, Ish=22.8 uA 3
Efficiency
1
0.75
0.5
0.25
0
200
400
600
chip1
chip2
chip3
chip4
chip5
chip6


50percent values across the chips, M1 with det Ipre=165.6 uA, Ish=22.8 uA 3fC
Gain (mV/fC)
Channel no.
0
50
100
150
200
250
300
350
400
450
500
550
600
650
700
750


Noise across the chips, M1 with det Ipre=165.6 uA, Ish=22.8 uA 3fC
ENC
Channel no.
0
10
20
30
40
50
50
100
150
200
250
300
350
400
450
500
550
600
650
700
750


Gains and noises across the hybrid, M1 with det Ipre=165.6 uA, Ish=22.8 uA 3fC
Gain (mV/fC)
0
20
40
60
80
100
120
140
160
180
200
100
200
300
400
500
600
700
Channel no.


Noise (mV)
0
5
10
15
20
25
30
35
40
45
50
100
200
300
400
500
600
700
Channel no.