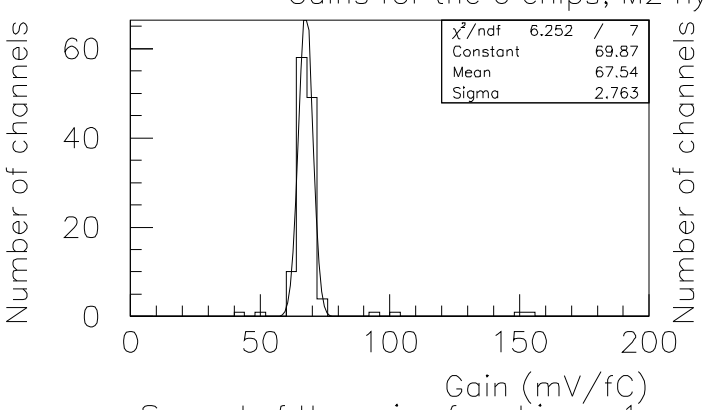
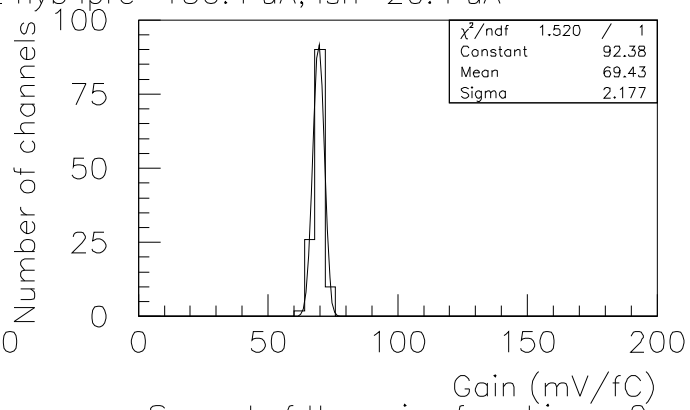
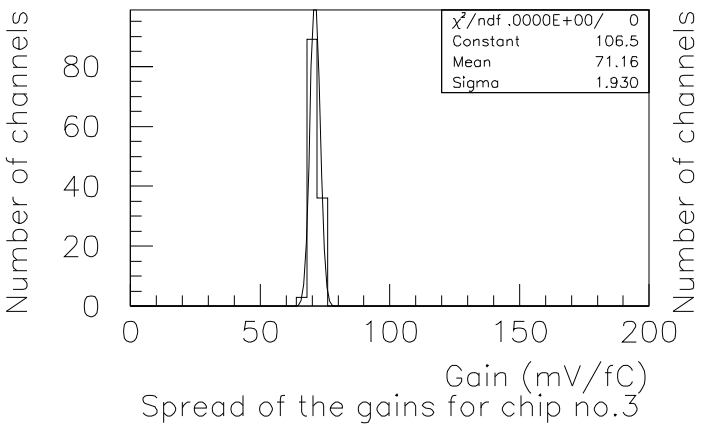
Gains for the 6 chips, M2 hyb Ipre=156.4 uA, Ish=20.4 uA



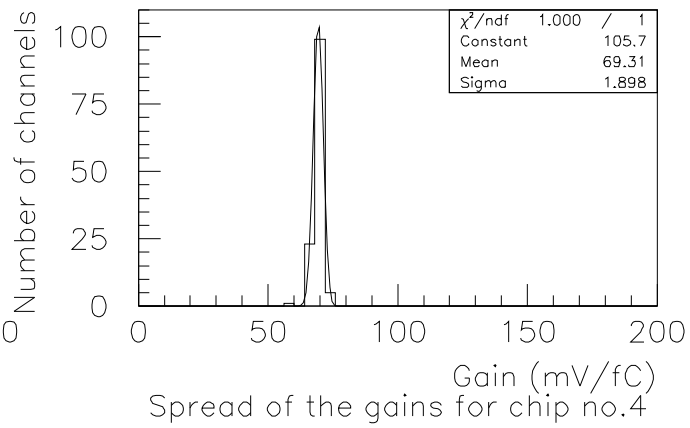
Spread of the gains for chip no.1



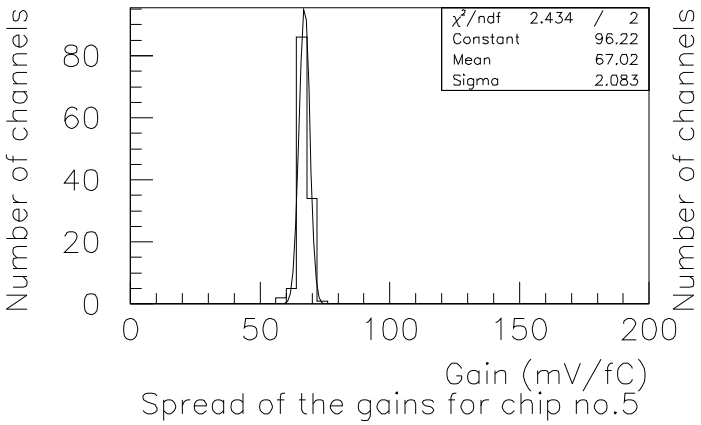
Spread of the gains for chip no.2



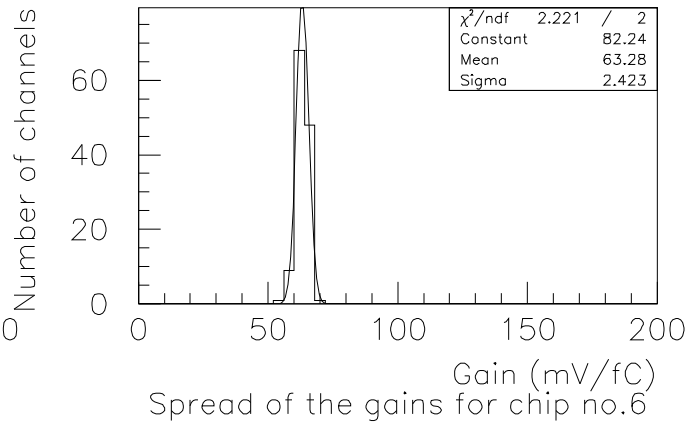
Spread of the gains for chip no.3



Spread of the gains for chip no.4

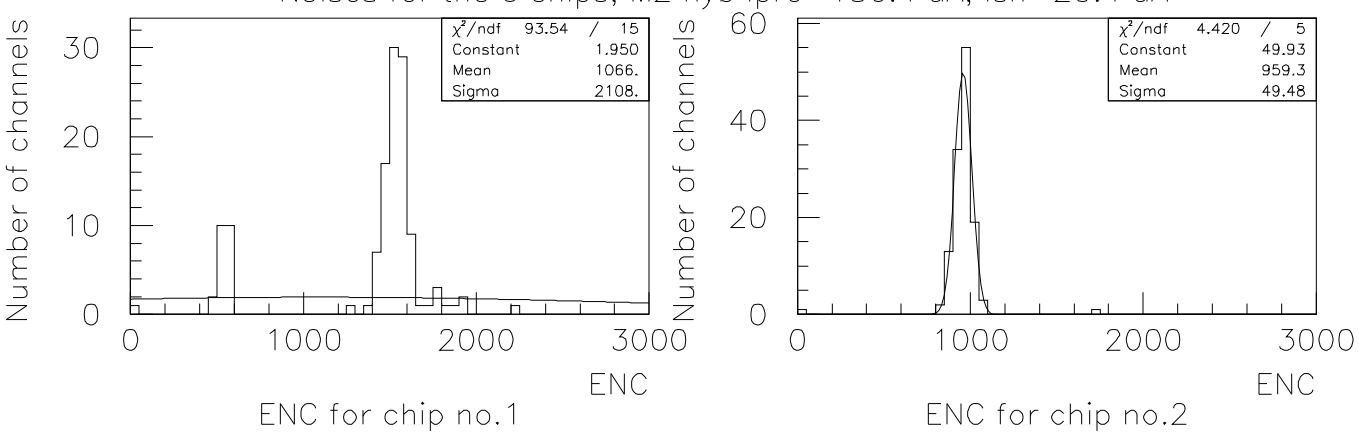


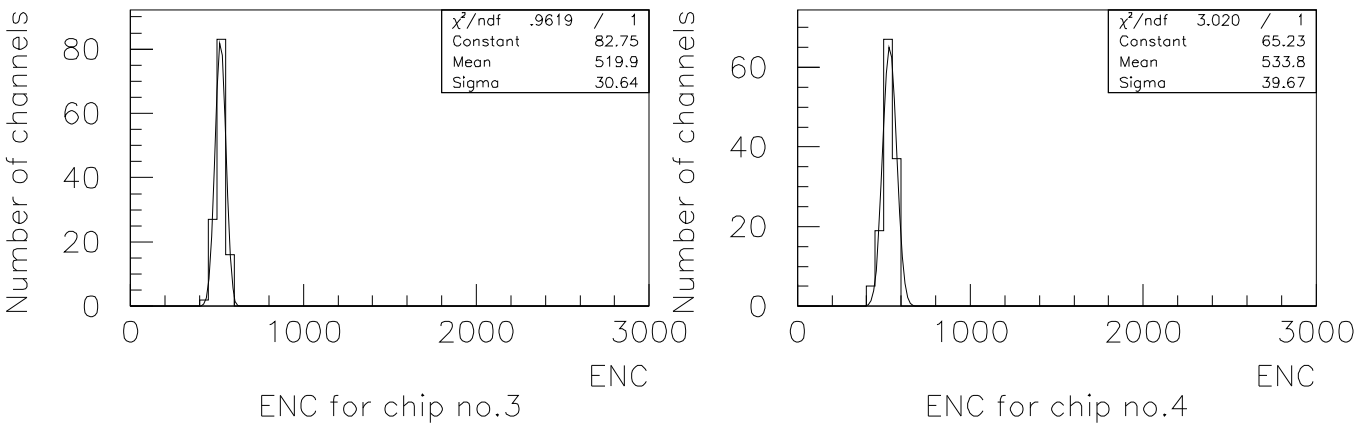
Spread of the gains for chip no.5

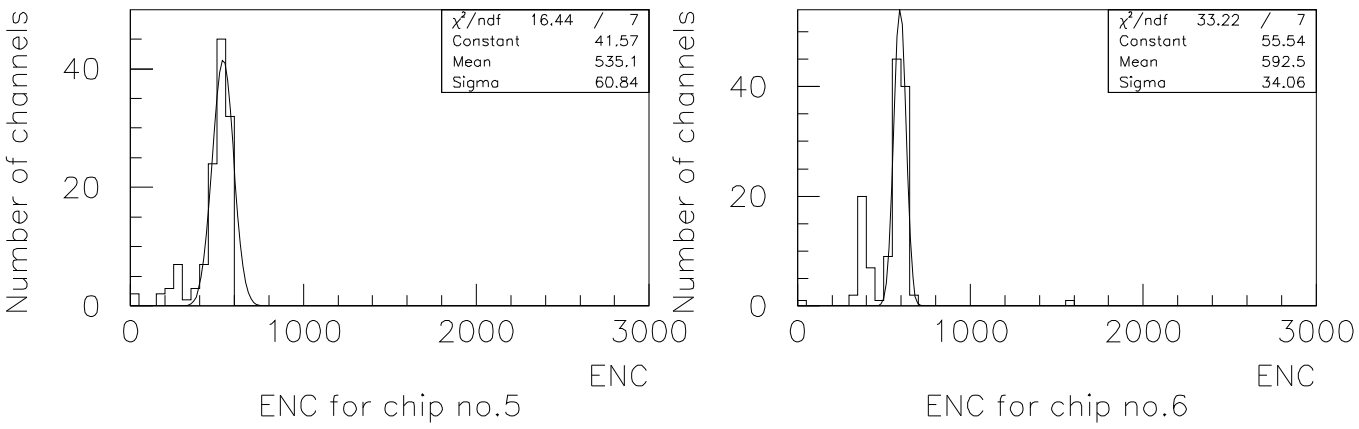


Spread of the gains for chip no.6

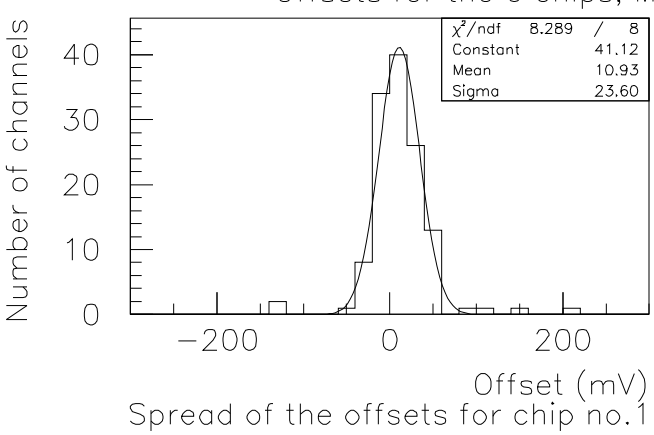
Noises for the 6 chips, M2 hyb Ipre=156.4 uA, Ish=20.4 uA



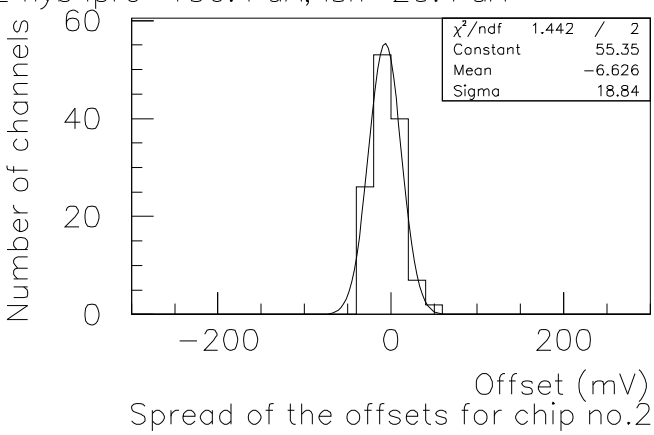




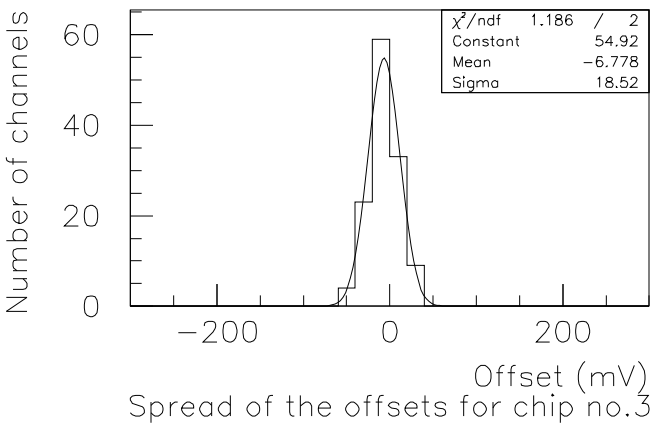
Offsets for the 6 chips, M2 hyb Ipre=156.4 uA, Ish=20.4 uA



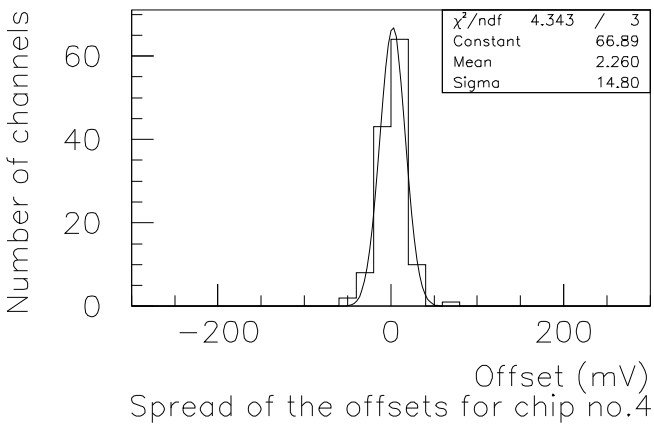
Spread of the offsets for chip no.1



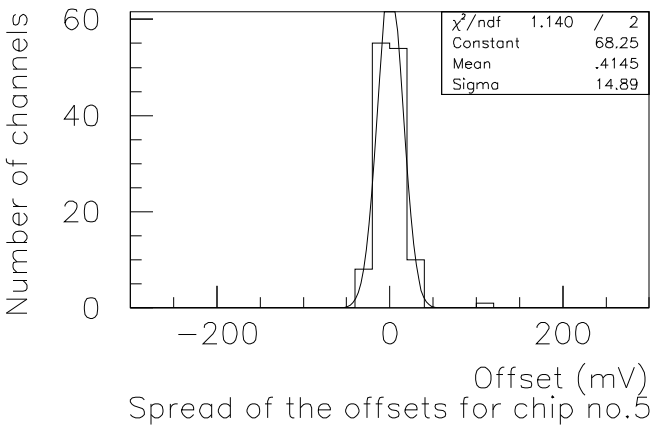
Spread of the offsets for chip no.2



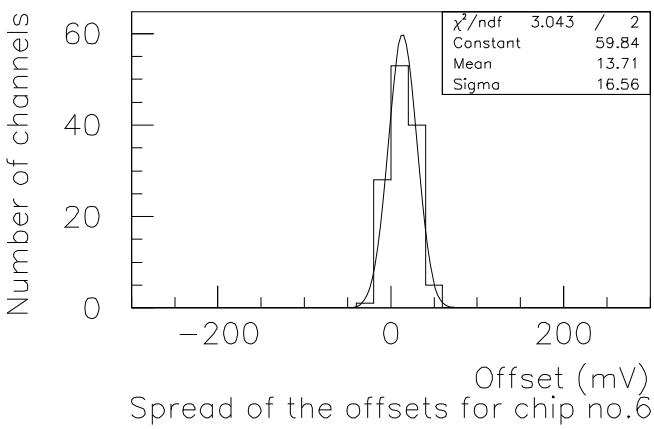
Spread of the offsets for chip no.3



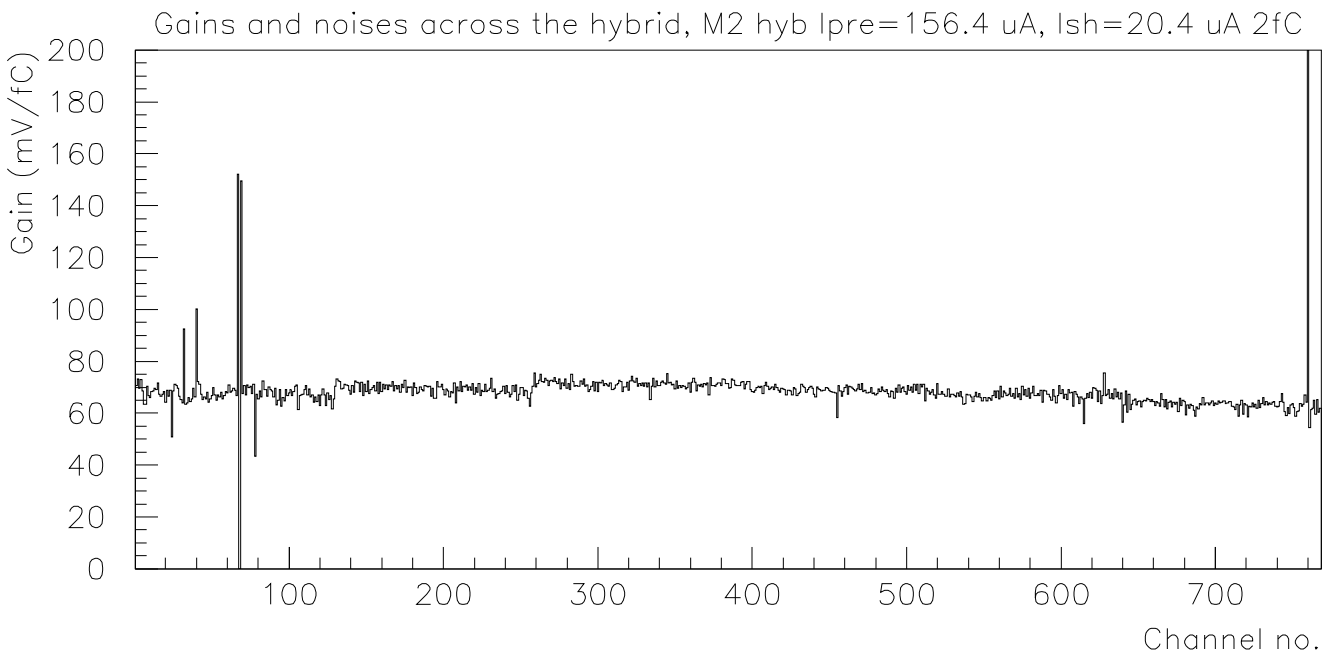
Spread of the offsets for chip no.4

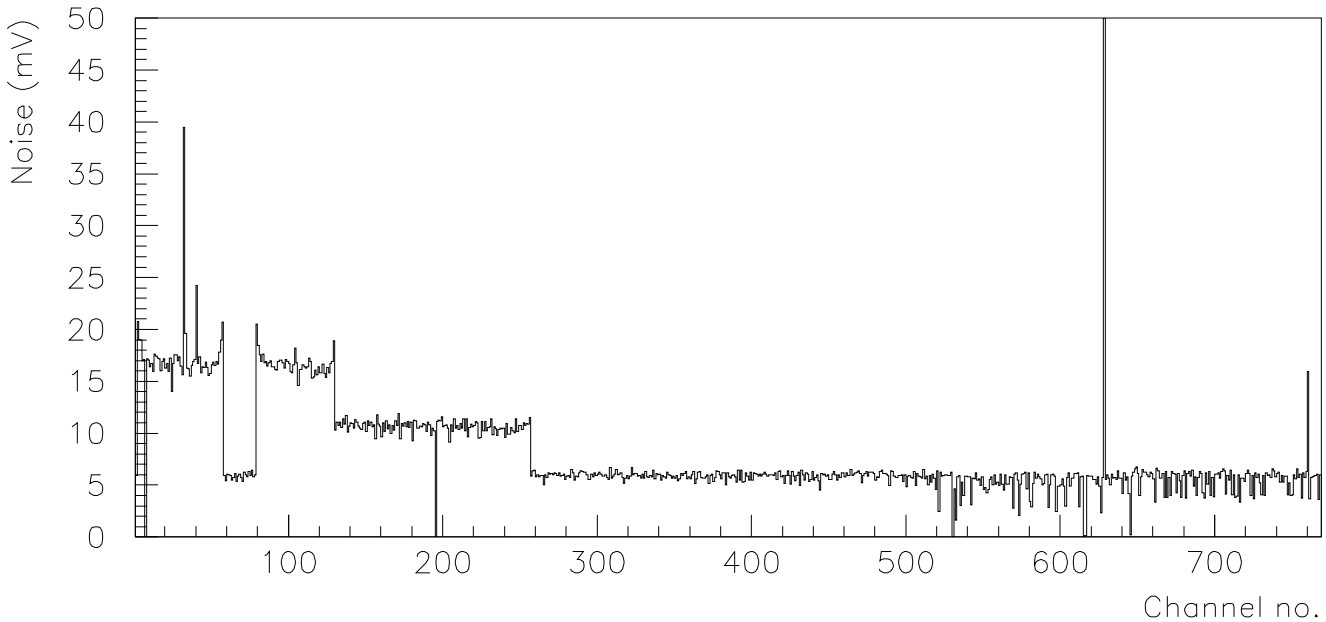


Spread of the offsets for chip no.5



Spread of the offsets for chip no.6


Gains and noises across the hybrid, M2 hyb Ipre=156.4 uA, Ish=20.4 uA 2fC
Gain (mV/fC)
0
20
40
60
80
100
120
140
160
180
200
100
200
300
400
500
600
700
Channel no.


Noise (mV)
0
5
10
15
20
25
30
35
40
45
50
100
200
300
400
500
600
700
Channel no.